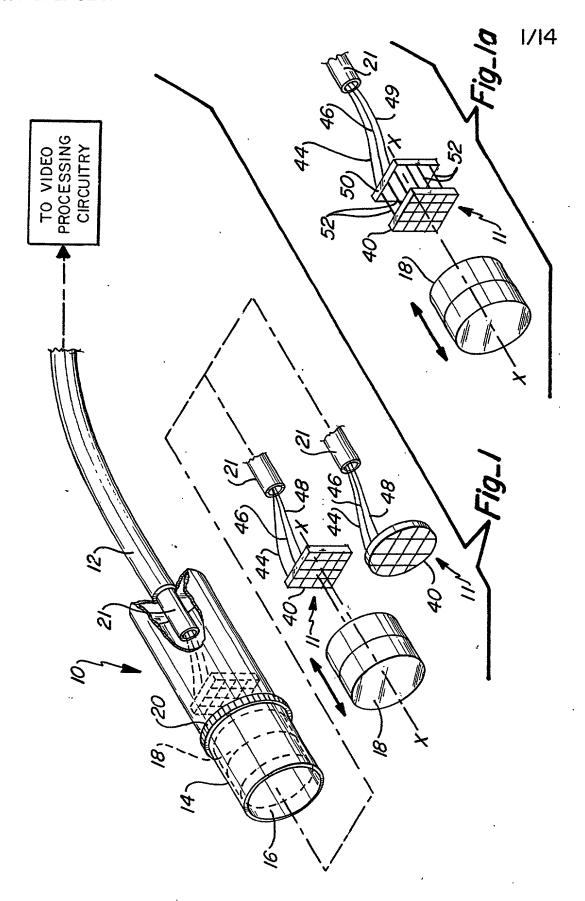
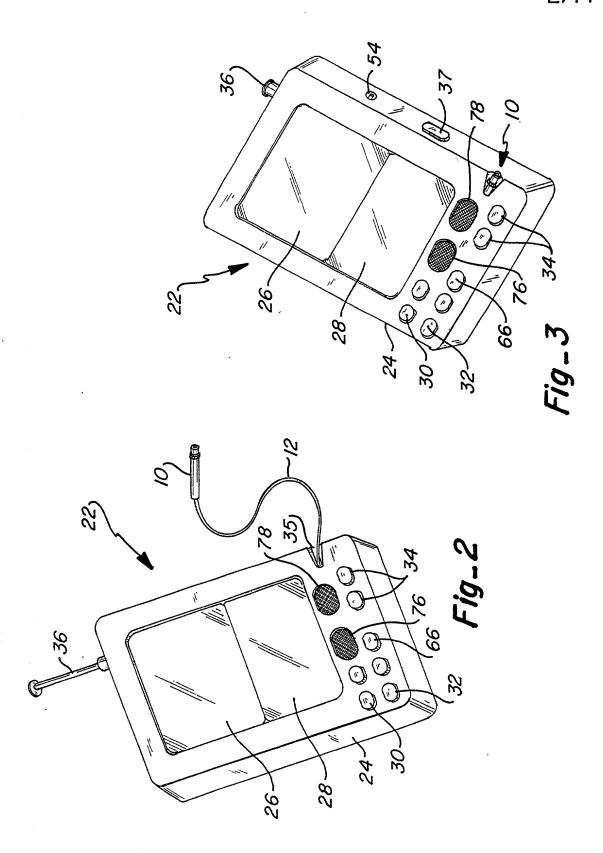
U.S. Continuation-in-Part Patent Application Entitled "HAND-HELD COMPUTERS INCORPORATING REDUCED AREA IMAGING DEVICES" to Edwin L. Adair, et al.; Our File No. 7018-23-CIP10

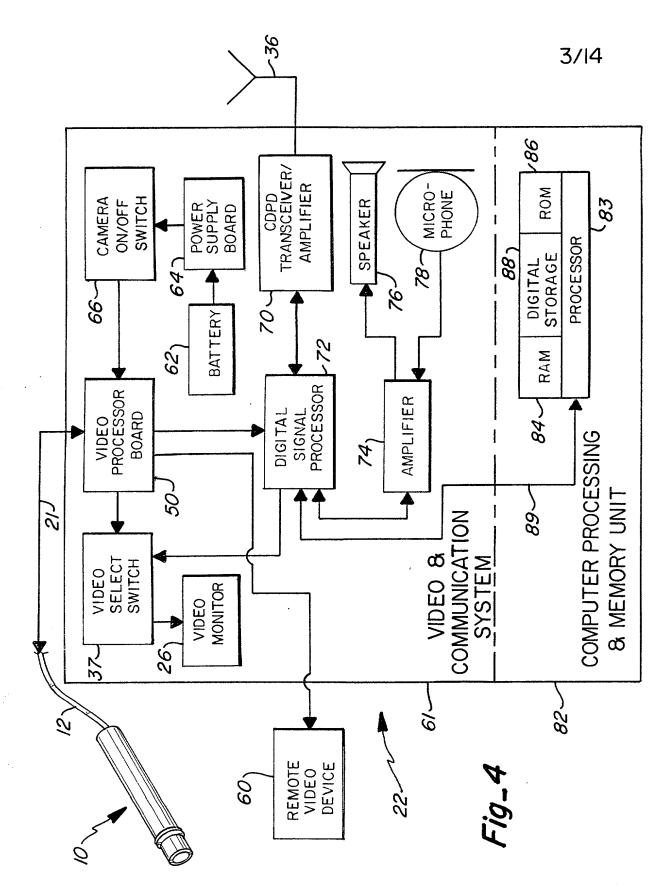


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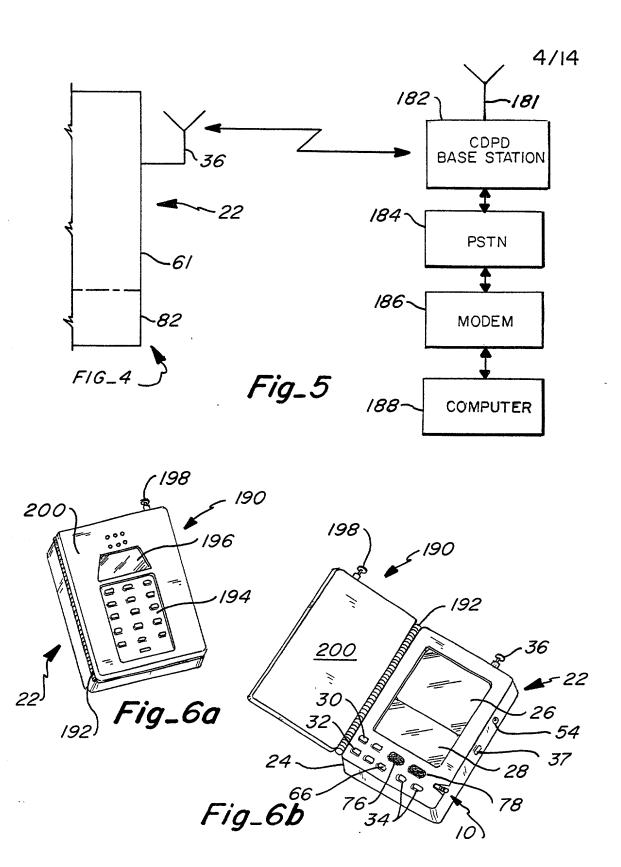


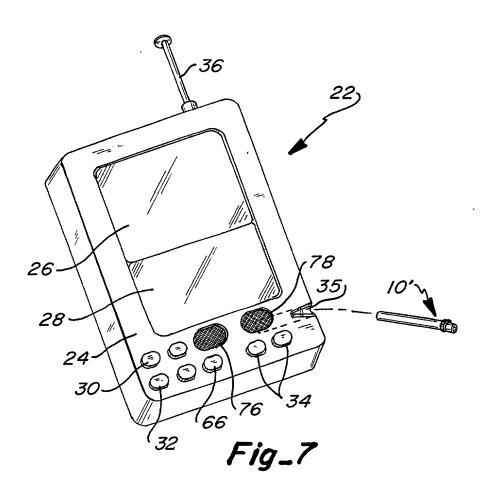
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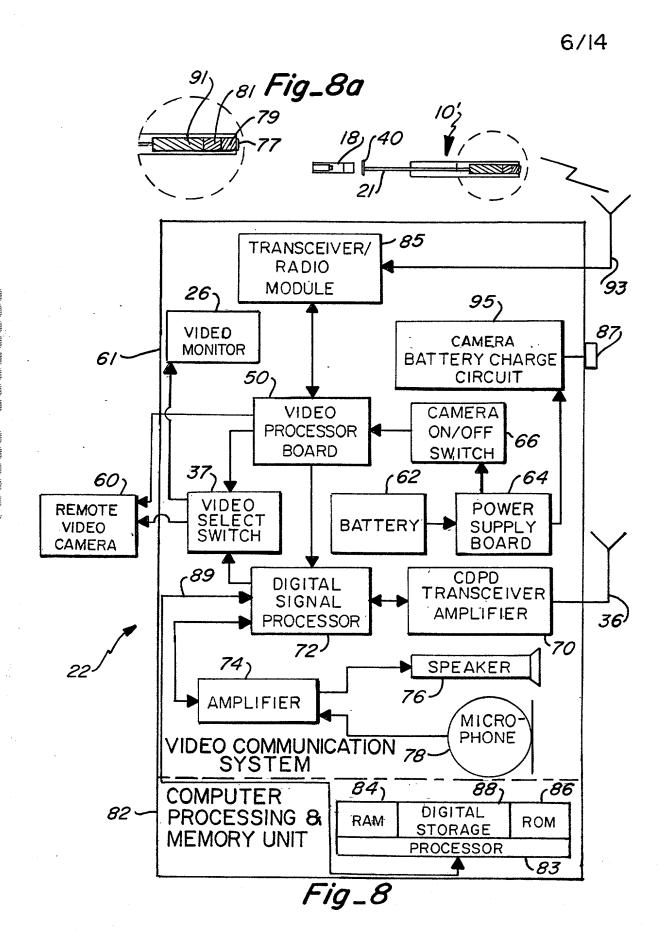


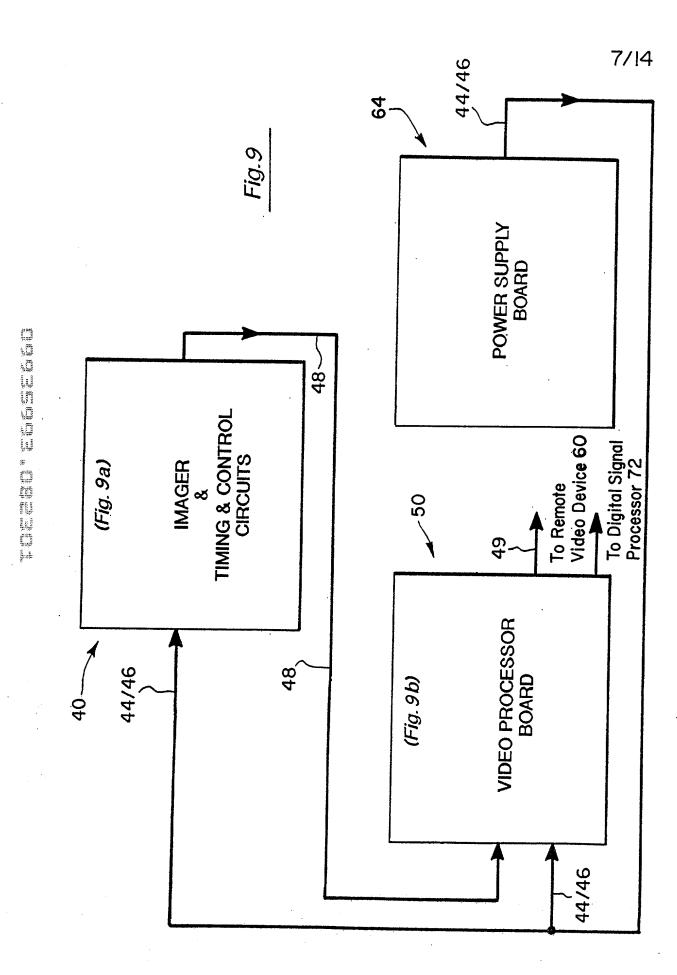


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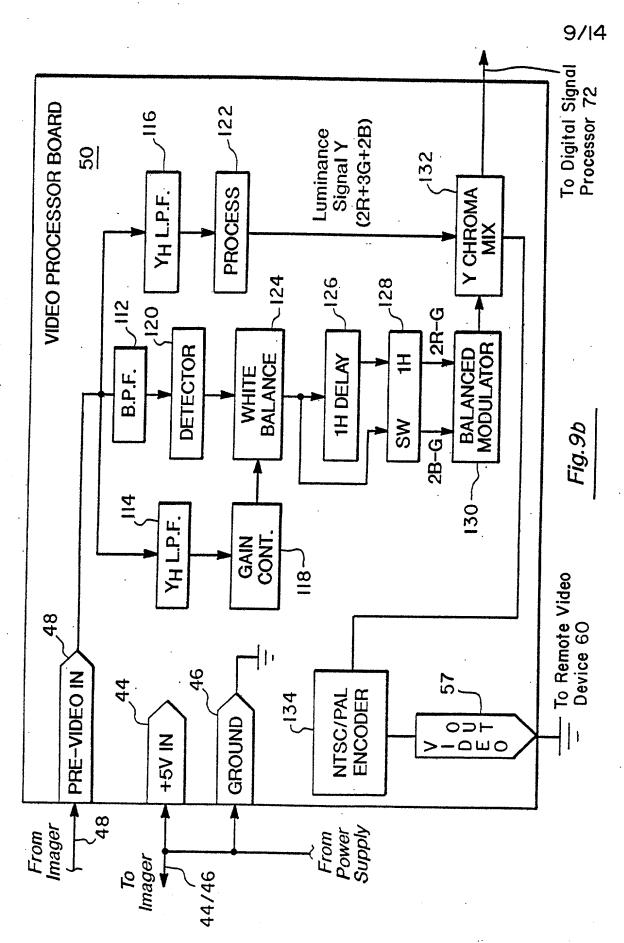




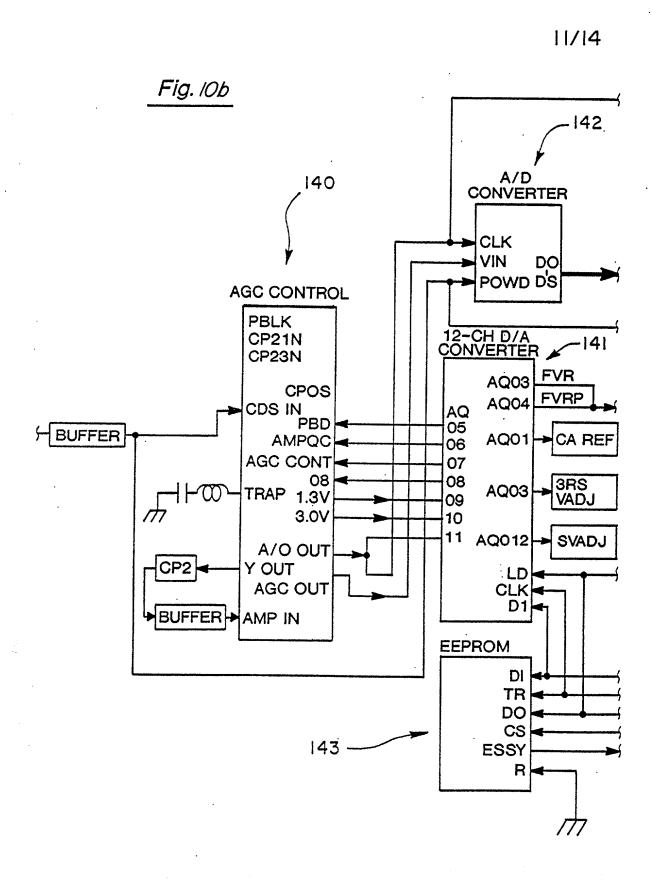


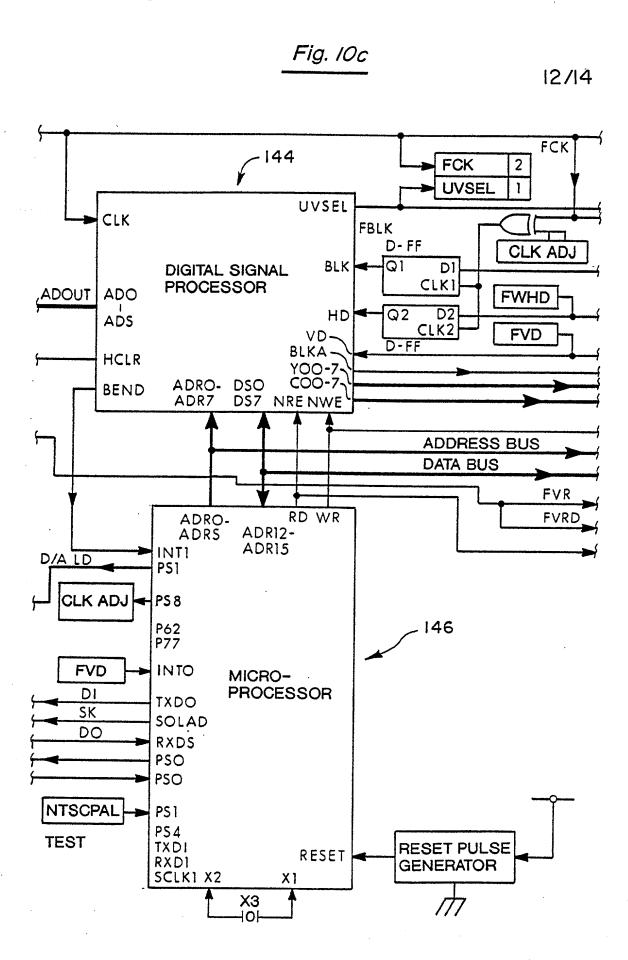
To Video Processor Analog out 8/14 48 901~ 7104 108 ARRAY OF PIXELS IMAGER & TIMING & CONTROL CIRCUITS 90 DECODER COUNTER LATCHES COLUMN SIGNAL COND. 00 AND CONTROL CIRCUITS TIMING 92 O & -> W &. 98 **ОШОООШК** RUN DATA CLOCK INPUT DEFAULT 96 OODZH EE GROUND 94 +5V IN SEHCHA From Power Supply Board Fig. 9a

U.S. Continuation-in-Part Patent Application Entitled "HAND-HELD COMPUTERS INCORPORATING REDUCED AREA IMAGING DEVICES" to Edwin L. Adair, et al.; Our File No. 7018-23-CIP10

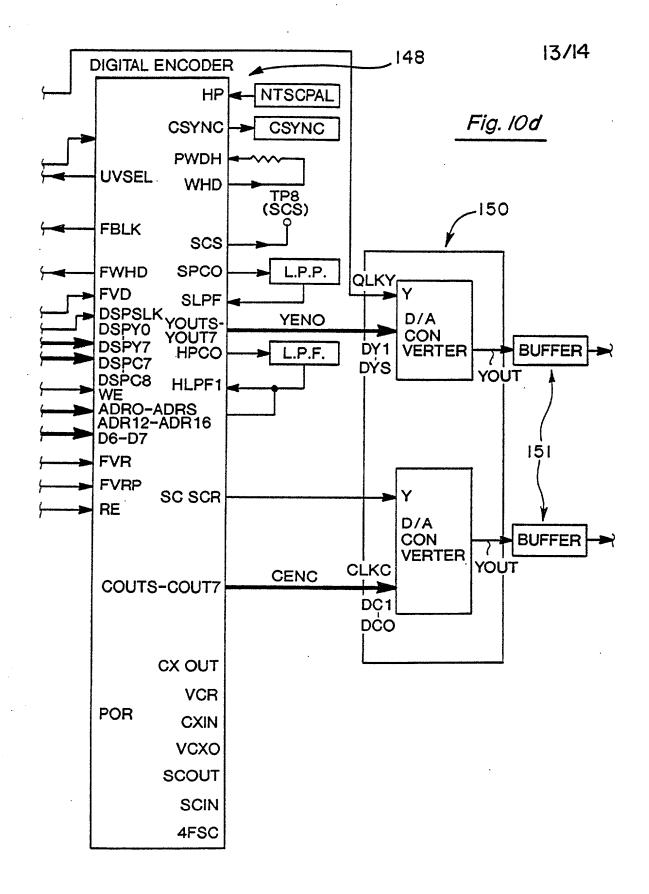


10/14 SHP SHD S/H **BIAS** BLK IC CDS PRE-VIDEO IN +5V OUT GND





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